Execution trace and memory analysis

Featuring Pin and Protobuf

Matthieu Tardy

EPITA 2017

July 17, 2015

Introduction

Motivation

- Discover PIN API
- Overview of performances and scalability
- Simplify reverse engineering

What is an execution trace?

- Log of memory accesses
- Read/Write
- Mappings changes (mmap/remap/unmap)

Why?

- Code optimization
- Reverse engineering
- Visualization

Optimization

- Benchmarks
- Global memory usage
- Structure accesses

Reverse engineering

Exemple

Statically linked binaries

- Include libraries
- Huge amount of code

Possible approaches

- Full reverse
- Code tracing
- Signatures - IDA FLIRT
- Memory access tracing

Methods

Linux

- ptrace
- Valgrind IR

Windows

- Debugging API

Both

- Pin
- Static analysis

Why Pin?

- Windows / Linux
- Faster

Pin

Instrumentation

What is it?

Inserting code in the program workflow.

Instrumentation


```
xor     %eax,%eax
push    %eax
push    $0x68732f2f
push    $0x6e69622f
mov     %esp,%ebx
push    %eax
push    %ebx
mov     %esp,%ecx
mov     $0xb,%al
int     $0x80
```

Figure : Normal code

Instrumentation


```
xor    %eax,%eax
<instrumentation code>
push   %eax
<instrumentation code>
push   $0x68732f2f
<instrumentation code>
push   $0x6e69622f
<instrumentation code>
mov    %esp,%ebx
<instrumentation code>
push   %eax
<instrumentation code>
push   %ebx
<instrumentation code>
mov    %esp,%ecx
<instrumentation code>
mov    $0xb,%al
<instrumentation code>
int    $0x80
<instrumentation code>
```

Figure : Instrumented code

Features

- Instrumentation framework
- IA32 & x86_64
- JIT Mode (JIT recompiling)
- Probe mode (Code trampolines)
- Doesn't affect application state
- Handle dynamically generated code
- Attach to running process

How?

1. Generate a dynamic library (pintool)
2. Use pin to start / attach the process and inject the pintool
3. Process is now instrumented

Core

Core

Goal

Record all the information we need at runtime to form the execution trace.

Memory access monitoring


```
VOID LEVEL_PINCLIENT::INS_AddInstrumentFunction(INS_INSTRUMENT_CALLBACK fun, VOID* val);
UINT32 LEVEL_CORE::INS_MemoryOperandCount(INS ins);
VOID LEVEL_PINCLIENT::INS_InsertPredicatedCall(INS ins, IPOINT ipoint, AFUNPTR funptr,...);
BOOL LEVEL_CORE::INS_MemoryOperandIsRead(INS ins, UINT32 memopIdx);
BOOL LEVEL_CORE::INS_MemoryOperandIsWritten(INS ins, UINT32 memopIdx);
```

Guideline

1. Define an instruction callback function
2. Insert an instrumentation function if the instruction access to memory
3. Record the memory access

Memory mapping monitoring


```
VOID LEVEL_PINCLIENT::PIN_AddSyscallEntryFunction(SYSCALL_ENTRY_CALLBACK fun, VOID* val);
VOID LEVEL_PINCLIENT::PIN_AddSyscallExitFunction(SYSCALL_EXIT_CALLBACK fun, VOID* val);
```

Guideline

1. Define a syscall entry callback function
2. Record the syscall number
3. Define a syscall exit callback function
4. Check recorded syscall number and dump mappings if mmap/unmap/remap

Protobuf

What is protocol buffers?

Method to serialize structured data

> *Think XML, but smaller, faster, and simpler.*
>
> – *Google*

Features

- Fast
- Flexible
- Easy to use
- Bindings for a lot of languages
- Adapted to large sets of small data

What does it look like?


```
message MemoryAccess
{
    enum AccessType
    {
        READ = 1;
        WRITE = 2;
    }

    required uint64 time = 1;
    required uint64 ins_addr = 2;
    required uint64 mem_addr = 3;
    required uint32 size = 4;
    required AccessType access_type = 5;
}
```

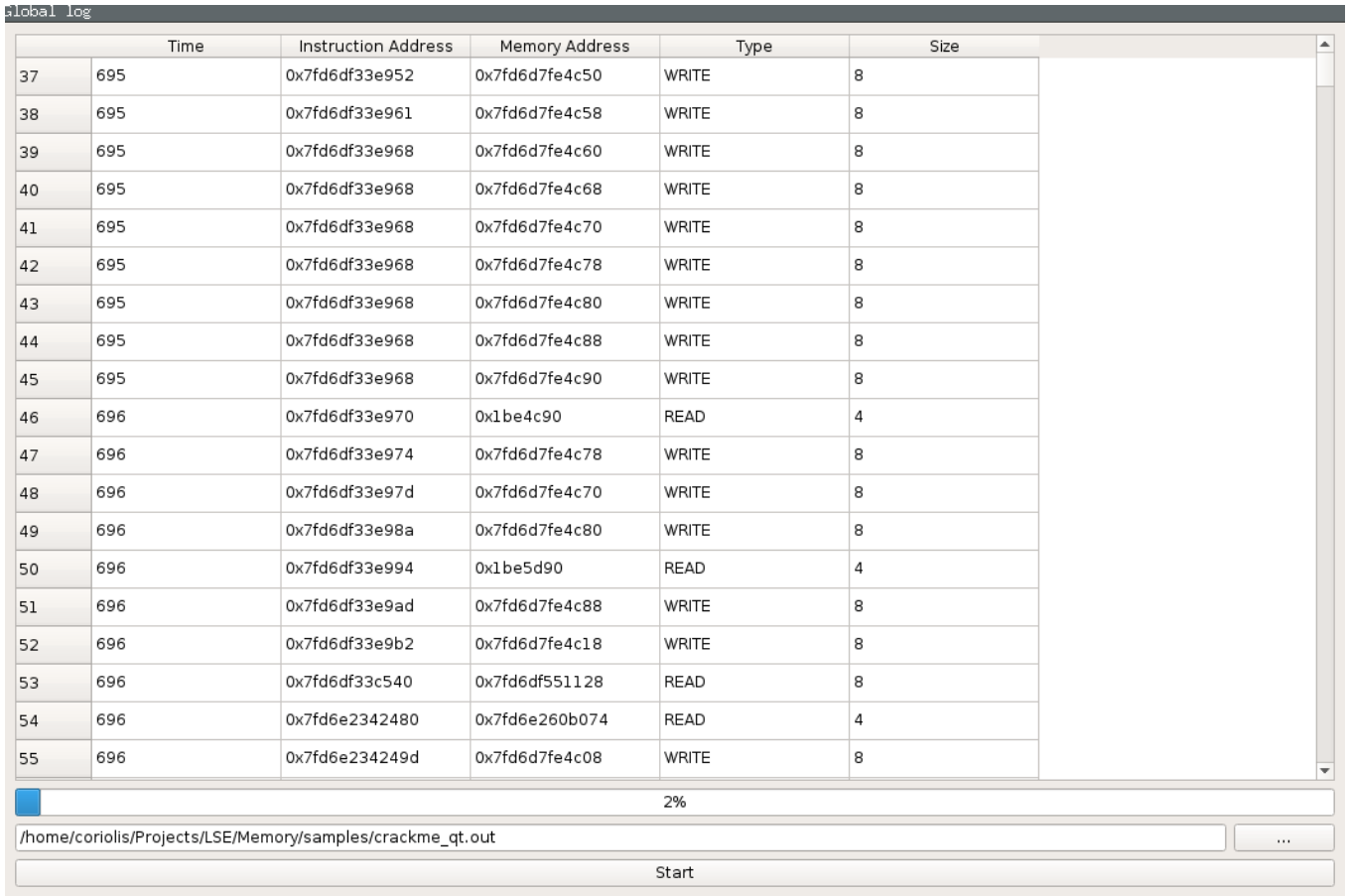
Analyzer

Analyzer

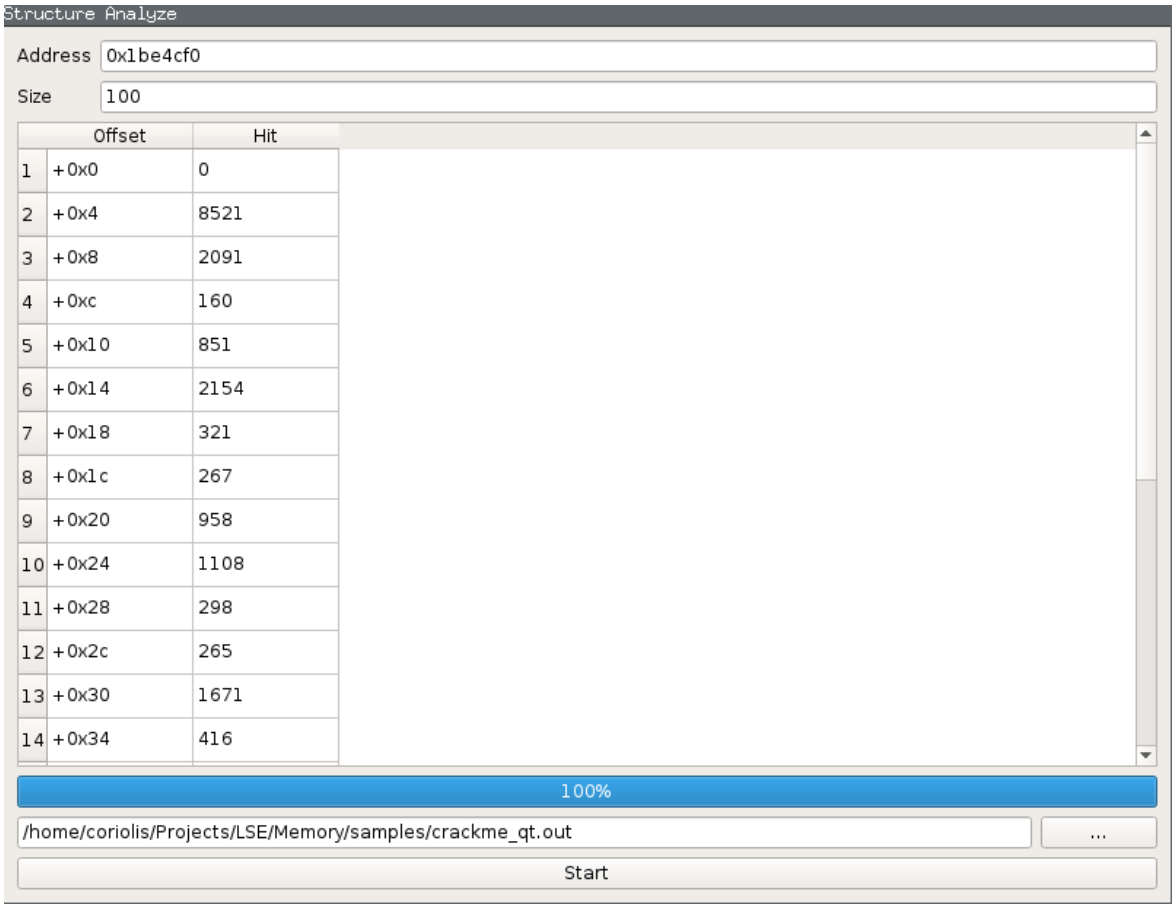
Goal

Analyze the informations in the execution trace and display it with a usable representation.

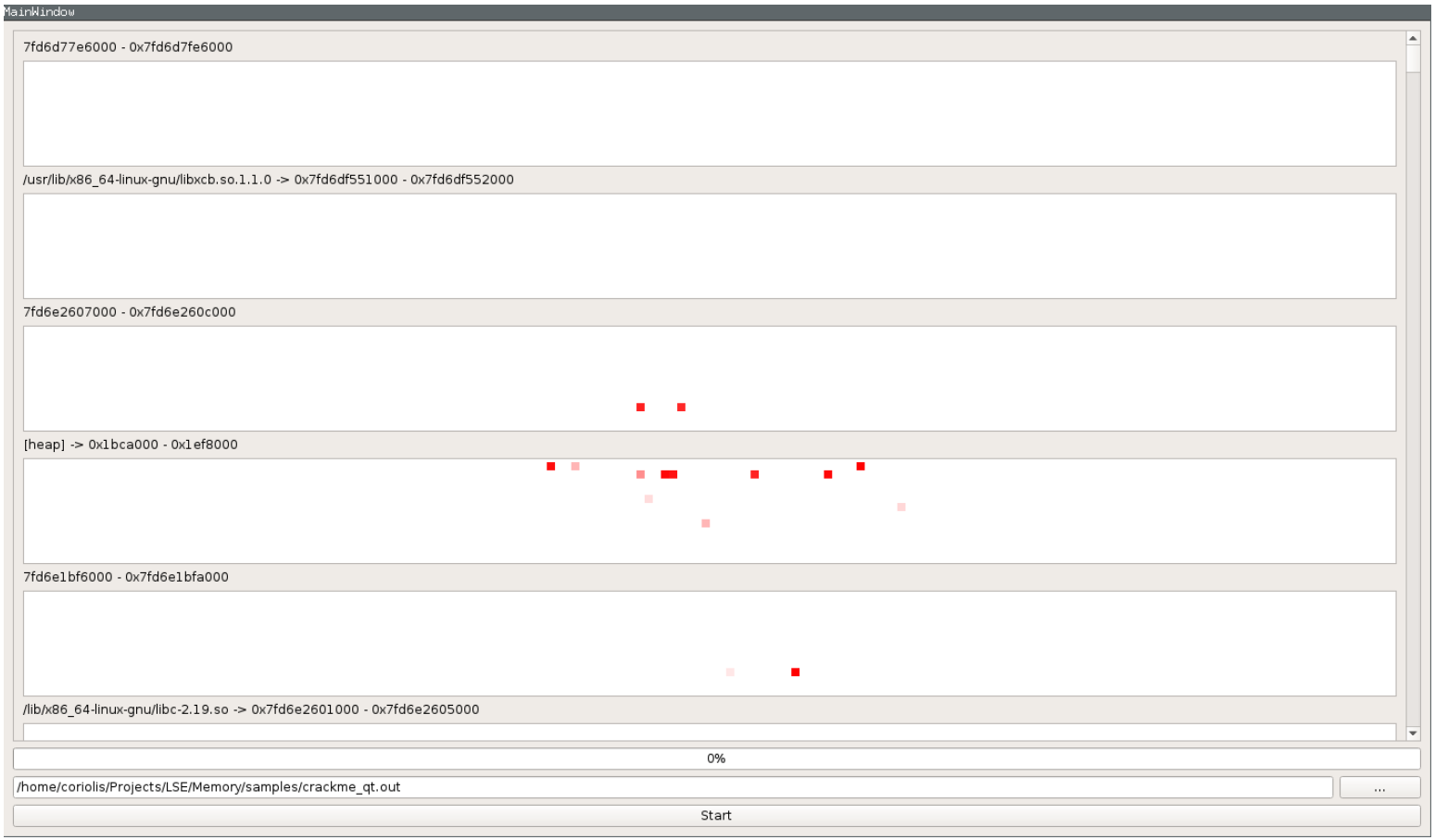
Basic log



Structure analysis



Dynamic Heatmap



Problems

- A *LOT* of data
  - Space problem
  - Speed problem
- Visualization isn't useful
  - Too much noise

What's the point then?

State

With this design it's not really usable.

But

- Logging using Pin is still the fastest way to have an execution trace of the *code*.
- Moving the analysis in the pintool allows us to do very efficient access analysis on structures or small areas.

What could be interesting

- DWARF Handling
- Code tracing

Conclusion

- Huge API, lot of features
- Not adapted for logging
- Viable for stats
- Viable for real instrumentation
  - Modify internal states registers
  - Modify functions

Tips

- Not compatible with 4.0 kernel
- Not compatible with gcc $>$ 4.9
- Be careful with multithreaded applications
- Use PIN Lock API
- Keep data processing in pintools

Outroduction

References

- Pin User Guide
- Pin presentation
- Protocol Buffers
- Malware unpacking with pin

Contact

- *Email*: coriolis@lse.epita.fr
- *Twitter*: @_coriolis_
- *IRC*: coriolis@Rezosup.org

Questions