

fulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

▲□▶▲□▶▲□▶▲□▶ □ のQ@

mikro - Efficient Inter Process Communication

Julien Freche & Victor Apercé

julien.freche@lse.epita.fr viaxxx@lse.epita.fr http://lse.epita.fr/

Outline I



2 Inter Process Communication

3 Implementation

- Linux
- Mach
- L4
- seL4

4 mikro - Implementation

- Existing enhancementsmikro innovation
- 5 Conclusion



mikro - Efficient Inter Process Communication

ulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ 三三 - のへで

Introduction



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

・ロト・日本・モト・モー シックの

Most of the processes want to interact with each others.

- Several ways to do it
- Very important in a micro-kernel



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ 三三 - のへで

Inter Process Communication

There is several ways to interact with other processes:

- File
- Signal
- Socket
- Message queue
- Pipe
- Shared memory
- ...



mikro - Efficient Inter Process Communication

Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

・ロト・日本・日本・日本・日本・日本



Ulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

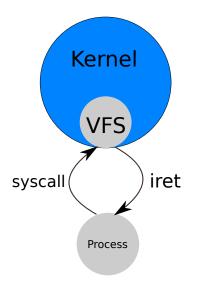
Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ - 三 - のへで

IPC purposes:

- Information sharing
- Modularity
- Convenience
- Privilege Separation

monolithic





mikro - Efficient Inter Process Communication

Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

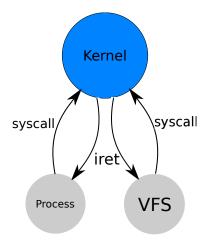
mikro -Implementation

Conclusion

- The process performs a syscall
- The kernel handles the request
- Execution of the process can continue

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ - 三 - のへで

micro kernel





mikro - Efficient Inter Process Communication

Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

< ロ > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < 回 > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > <

The process performs a syscall: send to the service

The kernel handles the

message to the service

The service handles the

request and performs a

The kernel transfer the answer to the process

Execution of the process

syscall: reply

can continue

request and send the

2

3

6



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

mplementation

Linu

Ma

L4

sel

・ロト・1日・1日・1日・1日・9900

mikro -Implementation

Conclusion

Implementation

There is two kind of message passing:

- Synchronous
- Asynchronous

Synchronous IPC are often considered faster:

• No copy from user to kernel required



mikro - Efficient Inter Process Communication

fulien Freche & Victor Apercé

Introduction

Inter Process Communication

Linux Mach

L4

sel.

▲□▶▲□▶▲□▶▲□▶ □ のQ@

mikro -Implementation

Conclusion





fulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

Linux

mikro -Implementation

Conclusion

◆□ > ◆母 > ◆臣 > ◆臣 > ○臣 - のへぐ

Linux

There is two IPC implementations on Linux

- System V IPC
- POSIX IPC

Each implementation contains:

- Message queues
- Semaphores
- Shared Memory

We will discuss about messages queues.



mikro - Efficient Inter Process Communication

ulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

Linu

L4

sel

▲□▶▲□▶▲□▶▲□▶ □ のQ@

mikro -Implementation

Conclusion



Iulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

Linu: Mach L4

sel

▲□▶▲□▶▲□▶▲□▶ □ のQ@

mikro -Implementation

Conclusion

• Older than POSIX IPCs

- Implemented using queues
- Use RCU: Read-Copy-Update
- Similar to a pipe but you have to send messages, not bytes
- Each message has a type, so you can filter messages
- Very portable



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

Linux Mach L4 seL4 mikro -Implementation

Conclusion

▲□▶▲□▶▲□▶▲□▶ □ のQ@

- Implemented as a special file system
- Each queue is a special file, you have to open it by its name
- Each message has a priority
- Message oriented



ulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

Linux

L4

sel

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ 三三 - のへで

mikro -Implementation

Conclusion

Mach



lulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation Linux Mach L4

seL

▲□▶▲□▶▲□▶▲□▶ □ のQ@

mikro -Implementation

Conclusion

• Port oriented

- Message transfer is asynchronous
- Similar to BSD sockets
- Rights are associated with ports
- Message oriented (header+body+trailer)
- Slow implementation ?



Iulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

Mac

L4

sel

mikro -Implementation

Conclusion

・ロト・(部・・ヨ・・ヨ・ うへぐ

L4

There is two types of synchronous IPC:

- Fast IPC
 - Very fast, use only CPU registers
 - During the syscall registers are preserved and execution is transfered to the receiver

A ロ ト 4 目 ト 4 目 ト 4 目 ・ 9 Q Q

- Can transfer only a limited amount of data
- Long IPC: two types
 - Using shared memory
 - Copy data from an address space to another



mikro - Efficient Inter Process Communication

Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation Linux Mach L4 seL4 mikro -

Implementation

Conclusion



ulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation Linux Mach

mikro -Implementation

Conclusion

◆□▶ ◆□▶ ◆∃▶ ◆∃▶ = のへで

seL4

- Endpoint oriented
- endpoint = small kernel object with a list of threads
- n receiver(s), n sender(s)
- Threads do not send a message to another thread but to an endpoint
- Make synchronous transfer when possible



Iulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation Linux Mach L4 seL4

mikro -Implementation

Conclusion

・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・
・

mikro - Implementation



mikro - Efficient Inter Process Communication

Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements mikro innovation

Conclusion

・ロト・日本・モト・モー シックや

mikro - Implementation



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Existing enhancements

mikro innovation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ 三三 - のへで

Existing enhancements



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Existing enhancements

mikro innovation

Conclusion

・ロト・日本・モト・モー シックの

- IPC in micro kernel are too slow
- Reducing the IPC time is hard
- mikro current implementation is too simple
- It will be changed soon to the new design introduced in these slides

What is reducing IPC speed?

- Ontext switch is slow:
 - by itself
 - because it invalidates TLB
- Opying message is slow
- Algorithms in send/receive system calls can be too slow
- Having a lot of intermediates between the sender and the final receiver



mikro - Efficient Inter Process Communication

fulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Existing enhancements

mikro innovation

Conclusion

・ロト・日本・日本・日本・日本・日本



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Existing enhancements

mikro innovation

Conclusion

・ロト・日本・モト・モー シックの

- Context switch time is quiet impossible to reduce
- For x86 processor: sysenter/sysexit (Intel) or syscall/sysret (AMD) are faster than standard interrupt handling
- This is the only thing that can be done

Copying message can be avoided by 2 means:

Shared pages

Sharing pages between the sender and receiver avoids any copy.

- Cannot be used for all IPC because it will starve memory
- Synchronisation between process is more complex
- Should be used only for large messages

Synchronous IPC

Synchronous IPC avoids to copy the message in kernel and then to its final destination.



mikro - Efficient Inter Process Communication

ulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Existing enhancements

mikro innovation

Conclusion



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Existing enhancements

mikro innovation

Conclusion

・ロト・日本・モト・モー シックの

• Our point of view is that's what can be improved

• That's quiet logical it's the only thing that is really under our control



fulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ 三三 - のへで

mikro innovation



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ - 三 - のへで

- IPC in L4 family (excluding seL4) have a log(n) cost, where n is the number of processes
- IPC are sent to a particular process, so finding the task struct of this process is log(n)

• mikro will do this in O(1)!



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ - 三 - のへで

- IPC in L4 family (excluding seL4) have a log(n) cost, where n is the number of processes
- IPC are sent to a particular process, so finding the task struct of this process is log(n)
- mikro will do this in O(1)!

- mikro IPC will have a connection support, named channel
- Every connection is represented by a channel handle, named chandle
- A chandle is just like a file descriptor, an integer
- There's a limited number of chandles by process
- Chandle associated structures are stored in a fixed-size array in every process task structure



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

Conclusion



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro milovatioi

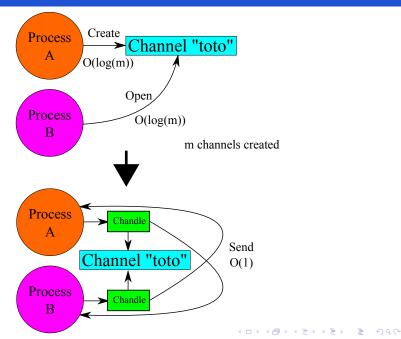
Conclusion

▲□▶▲□▶▲□▶▲□▶ □ のQ@

• Opening the connection costs O(log(n)) to find the receiver

- Chandle associated structure contains a pointer to this receiver
- Finding a chandle is looking in a fixed-size array
- Then send cost is O(1)

A little drawing





mikro - Efficient Inter Process Communication

Iulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovation

Conclusion



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovatior

Conclusion

▲□▶▲□▶▲□▶▲□▶ □ のQ@

- This is perfectly working for 1 to 1 connections
- But when the connection is client-server oriented (1 to N), this does not work
- Because we cannot suppose the fixed number of possible connection is enough for the server
- But we have a solution too!



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovation

Conclusion

In 1 to N:

- Creating the channel is almost the same as before
- But opening the channel is different:
 - No other chandle is created for the server
 - Client gets its chandle as usual
 - Server keeps track of these connections in its chandle associated structure

▲□▶▲□▶▲□▶▲□▶ □ のQ@



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

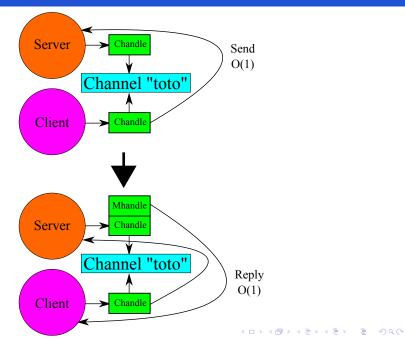
mikro innovation

Conclusion

- A client can then still send in O(1)
- When the server receives, a special chandle is created, named message handle or mhandle
- This mhandle is a temporary chandle that points to the sending client in order to reply
- The server must then reply to the client using this mhandle or close it

▲□▶ ▲□▶ ▲ 臣▶ ▲ 臣▶ 三臣 - のへで

A little drawing





mikro - Efficient Inter Process Communication

Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovation

Conclusion



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

Conclusion

- That way, client send in O(1) and server reply in O(1)
- But when a server want to send to a client directly: It costs O(log(n)) where n is the connection number to the server
- This isn't perfect but we think that kind of behaviour is not that much used

▲□▶▲□▶▲□▶▲□▶ □ のQ@

- mikro will also keep a small data for each client connected to a server channel
- When a client sends a message to the server, this data is provided to the server
- That way server can store a pointer in that data to get faster its own data associated to this client
- It avoids the server to do an extra log(n) to find the data associated with the client PID



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation Existing enhancements

mikro innovatioi

Conclusion

・ロト・日本・日本・日本・日本・日本



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ 三三 - のへで

Conclusion



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

- As said before, we haven't yet tested all this
- We'll do benchmarks when it'll be implemented to check that model
- So let's see you in winter to check if this idea was the good one

▲□▶ ▲□▶ ▲ 臣▶ ▲ 臣▶ 三臣 - のへで

Julien Freche

- julien.freche@lse.epita.fr
- @JulienFreche
- Victor Apercé
 - viaxxx@lse.epita.fr

Feel free to contact us. We will be happy to answer.



mikro - Efficient Inter Process Communication

ulien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

・ロト・日本・日本・日本・日本・日本



Julien Freche & Victor Apercé

Introduction

Inter Process Communication

Implementation

mikro -Implementation

Conclusion

▲□▶ ▲□▶ ▲ 三▶ ▲ 三▶ 三三 - のへで

Thank you for your attention