

Network stack in Kernel - Presentation

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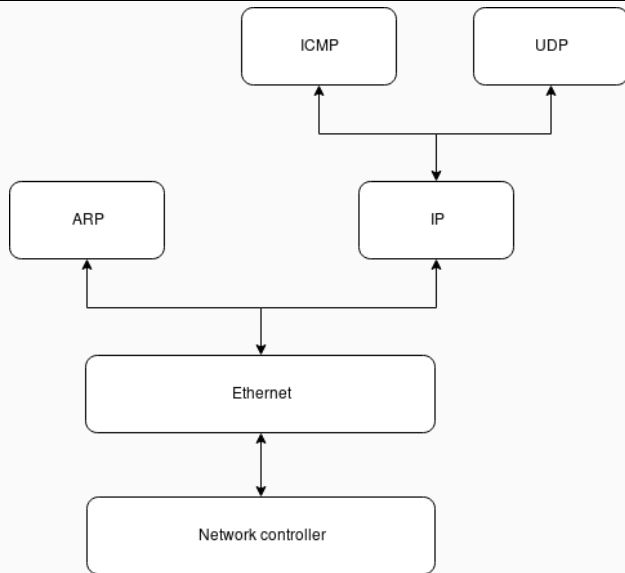


Figure 1: Network Stack

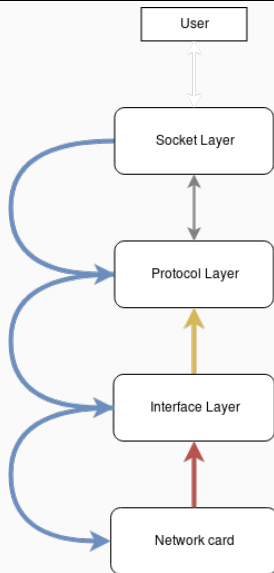


Figure 2: Interactions

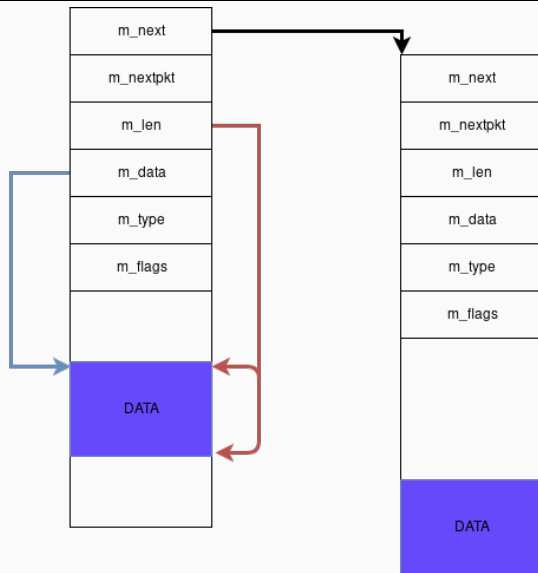


Figure 3: Mbuf chained

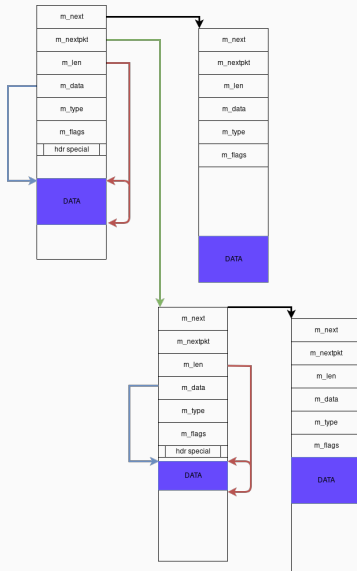


Figure 4: Mbuf chained headers

What is PCI?

31		16 15		0		
Device ID		Vendor ID				00h
Status		Command				04h
Class Code			Revision ID			08h
BIST	Header Type	Lat. Timer	Cache Line S.			0Ch
Base Address Registers						10h
						14h
						18h
						1Ch
						20h
Cardbus CIS Pointer						24h
Subsystem ID			Subsystem Vendor ID			28h
Expansion ROM Base Address						2Ch
Reserved				Cap. Pointer		30h
Reserved						34h
Max Lat.	Min Gnt.	Interrupt Pin	Interrupt Line			38h
						3Ch

Figure 5: PCI Header

Finding a device

```
for(bus = 0; bus < 256; bus++) {
    for(device = 0; device < 32; device++) {
        if(vendorID == 0xFFFF)
            continue;          // Device doesn't exist
        if (is_device(bus, device, 0))
            //Device is found
            return;
        headerType = getHeaderType(bus, device, function);
        if((headerType & 0x80) != 0) {
            /* It is a multi-function device, so check remaining functions */
            for(function = 1; function < 8; function++) {
                if(getVendorID(bus, device, function) != 0xFFFF && is_device(bus, device, function))
                    return;
            }
        }
    }
}
```

Many registers to use:

Name	Abbreviation	Value to use
Device Control	CTRL	CTRL_SLU
Receive/Transmit	TCTL / RCTL	BSIZE, SECRC, EN, ...
Base Address	_DBAL / _DBAH	Queue Base Address
Queue Length	RDLEN / TDLEN	Number of descriptors
Queue Head	RDH / TDH	First desc. index
Queue Tail	RDY / TDY	Last desc. index
Interrupt Mask	IMS	RXO, RXT0, LSC, TXDE, ...

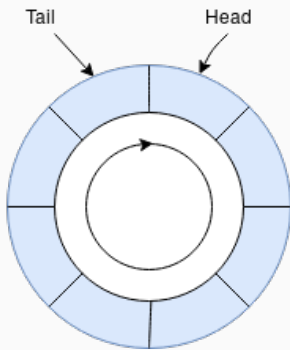


Figure 6: Ring Buffer Empty

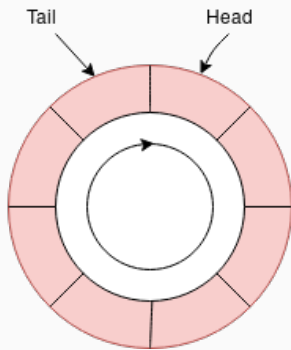


Figure 7: Ring Buffer Full

```
struct e1000_descriptor {
    u64 addr;
    u16 length;
    union {
        u16 checksum; /* RX */
        struct { /* TX */
            u8 cso;
            u8 cmd;
        };
    };
    u8 status;
    union {
        u8 errors; /* RX */
        u8 css; /* TX */
    };
    u16 special;
} __attribute__((packed));
```

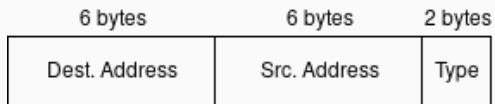


Figure 8: Ethernet Header

Vers.	H.L.	Type of Ser.	Length
ID			Fragment & Offset
TTL	Protocol		Checksum
Source IP Address			
Destination IP Address			

Figure 9: IP Header

Type	Code	Checksum
ID		Sequence Number
Originate Timestamp		
Receive Timestamp		
Transmit Timestamp		

Figure 10: ICMP Header

Source Port	Destination Port
UDP Length	Checksum

Figure 11: UDP Header

- Interface Kernel/Userland
- Store data to send and to receive
- Easily changed through syscalls
- One socket per distant IP

The only system calls that are important here are:

- bind
- gethost
- recvfrom
- sendto
- socket